

L Number	Hits	Search Text	DB	Time stamp
1	2479	programm\$4 near5 electronic\$1 near5 circuit\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/07/18 13:07
2	75	(programm\$4 near5 electronic\$1 near5 circuit\$1) and (chime\$1 or bell\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/07/18 13:07
3	19	((programm\$4 near5 electronic\$1 near5 circuit\$1) and (chime\$1 or bell\$1)) and capacitor\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/07/18 13:08
4	6	((((programm\$4 near5 electronic\$1 near5 circuit\$1) and (chime\$1 or bell\$1)) and capacitor\$1) and rc\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/07/18 13:13
5	48	rc same time\$1 near5 constant\$1 same duration\$1 same length\$1 same capacitor\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/07/18 15:14

L Number	Hits	Search Text	DB	Time stamp
1	2479	programm\$4 near5 electronic\$1 near5 circuit\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 13:07
2	75	(programm\$4 near5 electronic\$1 near5 circuit\$1) and (chime\$1 or bell\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 13:07
3	19	((programm\$4 near5 electronic\$1 near5 circuit\$1) and (chime\$1 or bell\$1)) and capacitor\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 13:08
4	6	((((programm\$4 near5 electronic\$1 near5 circuit\$1) and (chime\$1 or bell\$1)) and capacitor\$1) and rc\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 13:13

L Number	Hits	Search Text	DB	Time stamp
1	0	(cpu\$1 or controller\$1 or microcontroller\$1 or processor\$1 or microprocessor\$1) near5 rc\$1 and shitch\$2 and capacitor\$1 near5 (time\$1 near5 delay\$3 or length\$1 or duration\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 10:03
2	0	(cpu\$1 or controller\$1 or microcontroller\$1 or processor\$1 or microprocessor\$1) and rc\$1 and shitch\$2 and capacitor\$1 near5 (time\$1 near5 delay\$3 or length\$1 or duration\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 10:05
3	950	(cpu\$1 or controller\$1 or microcontroller\$1 or processor\$1 or microprocessor\$1) and rc\$1 and switch\$2 and capacitor\$1 near5 (time\$1 near5 delay\$3 or length\$1 or duration\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 11:48
4	51	((cpu\$1 or controller\$1 or microcontroller\$1 or processor\$1 or microprocessor\$1) and rc\$1 and switch\$2 and capacitor\$1 near5 (time\$1 near5 delay\$3 or length\$1 or duration\$1)) and voltage\$1 near5 square\$1 near5 wave\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 11:16
5	39	((cpu\$1 or controller\$1 or microcontroller\$1 or processor\$1 or microprocessor\$1) and rc\$1 and switch\$2 and capacitor\$1 near5 (time\$1 near5 delay\$3 or length\$1 or duration\$1)) and (chime\$1 or bell\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 11:43
6	22	((cpu\$1 or controller\$1 or microcontroller\$1 or processor\$1 or microprocessor\$1) and rc\$1 and switch\$2 and capacitor\$1 near5 (time\$1 near5 delay\$3 or length\$1 or duration\$1)) and (chime\$1 or bell\$1)) and programm\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 11:17
7	3	((cpu\$1 or controller\$1 or microcontroller\$1 or processor\$1 or microprocessor\$1) and rc\$1 and switch\$2 and capacitor\$1 near5 (time\$1 near5 delay\$3 or length\$1 or duration\$1)) and (chime\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 11:43
8	0	(pair\$1 or dual\$1 or several\$1 or plurality\$1) near5 capacitor\$1 near5 (time\$1 near5 delay\$3 or length\$1 or duration\$1) with chime\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 11:49
9	0	(pair\$1 or dual\$1 or several\$1 or plurality\$1) near5 capacitor\$1 near5 (time\$1 near5 delay\$3 or length\$1 or duration\$1) and chime\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 11:50
10	277	(pair\$1 or dual\$1 or several\$1 or plurality\$1) near5 capacitor\$1 near5 (time\$1 near5 delay\$3 or length\$1 or duration\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 12:08
11	0	((pair\$1 or dual\$1 or several\$1 or plurality\$1) near5 capacitor\$1 near5 (time\$1 near5 delay\$3 or length\$1 or duration\$1)) and chime\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 11:50
12	0	((pair\$1 or dual\$1 or several\$1 or plurality\$1) near5 capacitor\$1 near5 (time\$1 near5 delay\$3 or length\$1 or duration\$1)) and (cpu\$1 or microprocessor\$1 or processor\$1 or microcontroller\$1 or controller\$1) near5 rc\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 11:52

13	54	((pair\$1 or dual\$1 or several\$1 or plurality\$1) near5 capacitor\$1 near5 (time\$1 near5 delay\$3 or length\$1 or duration\$1)) and (cpu\$1 or microprocessor\$1 or processor\$1 or microcontroller\$1 or controller\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 12:10
14	16	((pair\$1 or dual\$1 or several\$1 or plurality\$1) near5 capacitor\$1 near5 (time\$1 near5 delay\$3 or length\$1 or duration\$1)) and (cpu\$1 or microprocessor\$1 or processor\$1 or microcontroller\$1 or controller\$1)) and rc\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 12:11
15	1167	capacitor\$1 near5 output\$1 near5 (time\$1 near5 delay\$3 or length\$1 or duration\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 12:09
16	1	(capacitor\$1 near5 output\$1 near5 (time\$1 near5 delay\$3 or length\$1 or duration\$1)) and chime\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 12:09
17	327	(capacitor\$1 near5 output\$1 near5 (time\$1 near5 delay\$3 or length\$1 or duration\$1)) and (cpu\$1 or microprocessor\$1 or processor\$1 or microcontroller\$1 or controller\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 12:11
18	99	((capacitor\$1 near5 output\$1 near5 (time\$1 near5 delay\$3 or length\$1 or duration\$1)) and (cpu\$1 or microprocessor\$1 or processor\$1 or microcontroller\$1 or controller\$1)) and rc\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 12:11
19	18	((capacitor\$1 near5 output\$1 near5 (time\$1 near5 delay\$3 or length\$1 or duration\$1)) and (cpu\$1 or microprocessor\$1 or processor\$1 or microcontroller\$1 or controller\$1)) and rc\$1 and (tone\$1 or sound\$1 or bell\$1 or chime\$1 or music\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 12:25
20	14	4110750.uref.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 12:34
21	10	4110750.uref. and (length\$1 or time\$1 near5 delay\$1 or duration\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 12:41
22	6	(4110750.uref. and (length\$1 or time\$1 near5 delay\$1 or duration\$1)) and capacitor\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 12:51
23	49	capacitor\$1 near5 (larger\$1 or greater\$1 or longer\$1 or exceed\$1 or surpass\$1) near5 (duration\$1 or length\$1 or delay\$3 near5 time\$1) near5 output\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 12:53
24	12	(capacitor\$1 near5 (larger\$1 or greater\$1 or longer\$1 or exceed\$1 or surpass\$1) near5 (duration\$1 or length\$1 or delay\$3 near5 time\$1) near5 output\$1) and (musical\$1 near5 note\$1 or tone\$1 or sound\$1 or music\$4 or chime\$1 or bell\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/07/18 13:01